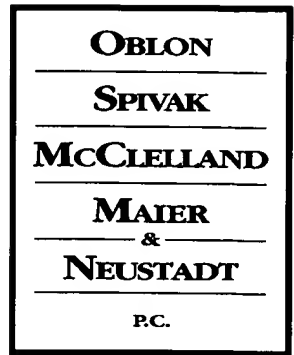


Docket No.: 211909US2CIP

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313



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RE: Application Serial No.: 09/915,396  
Applicants: Atsumi YAMAGUCHI, et al.  
RCE Filed: October 22, 2003  
For: METHOD OF MANUFACTURING  
SEMICONDUCTOR DEVICE  
Group Art Unit: 1765  
Examiner: Deo, Duy Vu Nguyen

SIR:

Attached hereto for filing are the following papers:

**RESPONSE**

Our check in the amount of -0- is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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211909US-2 CIP



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

:

ATSUMI YAMAGUSHI, ET AL.

: EXAMINER: DEO, DUY VU  
NGUYEN

SERIAL NO: 09/915,396

:

RCE      OCTOBER 22, 2003  
FILED:

: GROUP ART UNIT: 1765

FOR:      METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

**RESPONSE**

ASSISTANT COMMISSIONER FOR PATENTS  
ALEXANDRIA, VA 22313

SIR:

In response to the Office Action dated December 3, 2003, please consider the following remarks:

**Remarks/Arguments** begin on page 2 of this paper.